

Electronic Acknowledgement Receipt

EFS ID:	2601894
Application Number:	10620151
International Application Number:	
Confirmation Number:	8500
Title of Invention:	Digital clock recovery circuit employing fixed clock oscillator driving fractional delay line
First Named Inventor/Applicant Name:	Matthew A. Kliesner
Customer Number:	27975
Filer:	David L. Stewart/Lois Love
Filer Authorized By:	David L. Stewart
Attorney Docket Number:	72206
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Application Type:	Utility under 35 USC 111(a)

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Payment was successfully received in RAM	\$ 810
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Request for Continued Examination (RCE)	72206sb0030e_fill.pdf	746694	no	3
			b5a9811c465b8fc623eb93b123dc26e02e29dc82		

Warnings:

Information:

2		72206RespwithRCE.pdf	592077	yes	13
			35fdc5788672ad6d9oddd479fa02035bb8f9ace6		

Multipart Description/PDF files in .zip description

	Document Description	Start	End
	Amendment Submitted/Entered with Filing of CPA/RCE	1	1
	Claims	2	8
	Applicant Arguments/Remarks Made in an Amendment	9	13

Warnings:

Information:

3	Fee Worksheet (PTO-06)	fee-info.pdf	8211	no	2
			afa9170e978c8fbf021010dbc30edd8d8040cb8b		

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Total Files Size (in bytes):			1346982
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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

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